



Shinichi Takagi, The University of Tokyo

EDUCATION:

I) PhD (Electronic Engineering) : The university of Tokyo, 1987

II) MS (Electronic Engineering) : The university of Tokyo, 1984

III) Bachelor (Electronic Engineering) : The university of Tokyo, 1982

JOB CARRIERS:

2007.4 – present:

Professor, Department of Electronic Engineering, School of Engineering, The University of Tokyo

2004.4 – 2006.3:

Professor, Department of Frontier Informatics, Division of Transdisciplinary Sciences, Graduate School of Frontier Science, The University of Tokyo

2003.10 – 2008.3.:

Also working as Theme Leader, New Transistor Technology Theme, MIRAI Project, Guest Senior Research Scientist, National Institute of Advanced Industrial Science and Technology (AIST)

2003.10 -2004.3:

Professor, Department of Electronic Engineering, School of Engineering, The University of Tokyo

2001.8 – 2003.9:

Also working as Group Leader, New Transistor Technology Group, MIRAI Project, Association of Super-Advanced Electronics Technology (ASET),

1993.8 – 1995.3:

Visiting Research Scientist, Solid State Electronics Labs, Electrical Engineering Department, Stanford University (supervised by Professor James F. Gibbons and Dr. Judy L. Hoyt)

1987. 4- 2003.9:

Toshiba Research & Development Center, Toshiba Corporation