

**Abstract**

**INC-4**

## **Recent R&D Activities for Nanoelectronics in Japan**

**Hisatune Watanabe**

**In Japan, research and development on nanoelectronics have been carried out in variety of schemes, such as government-lead project, cooperation of industry and academia, and private programs. The author will introduce major government-lead nanoelectronics R&D activities in Japan, such as MIRAI-project, METI/Nanoelectronics project and MEXT/JST CREST project. Several R&D activities for nanoelectronics in Japan industry will also be presented.**

**From technological perspective, CMOS will continue to be a main stream even in nanoelectronics era. However, more focus on fusion of “More than Moore elements” or “Beyond CMOS elements” with CMOS should be implemented.**